

FIG. 1

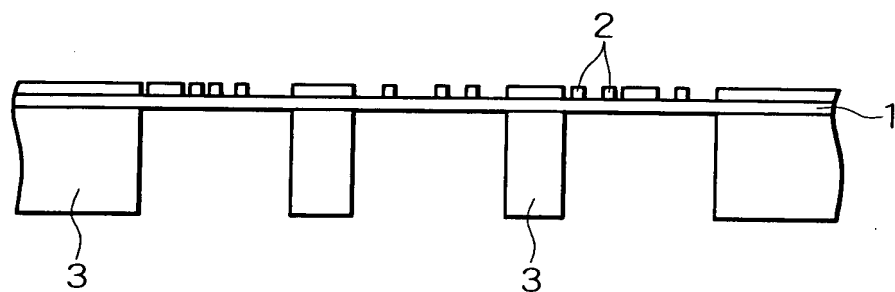


FIG. 2

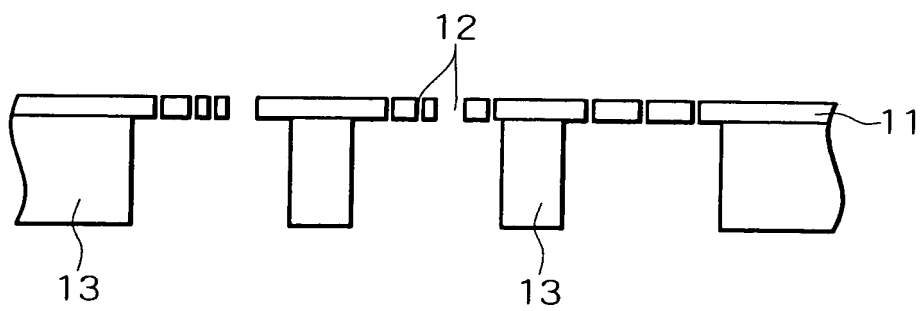


FIG. 3

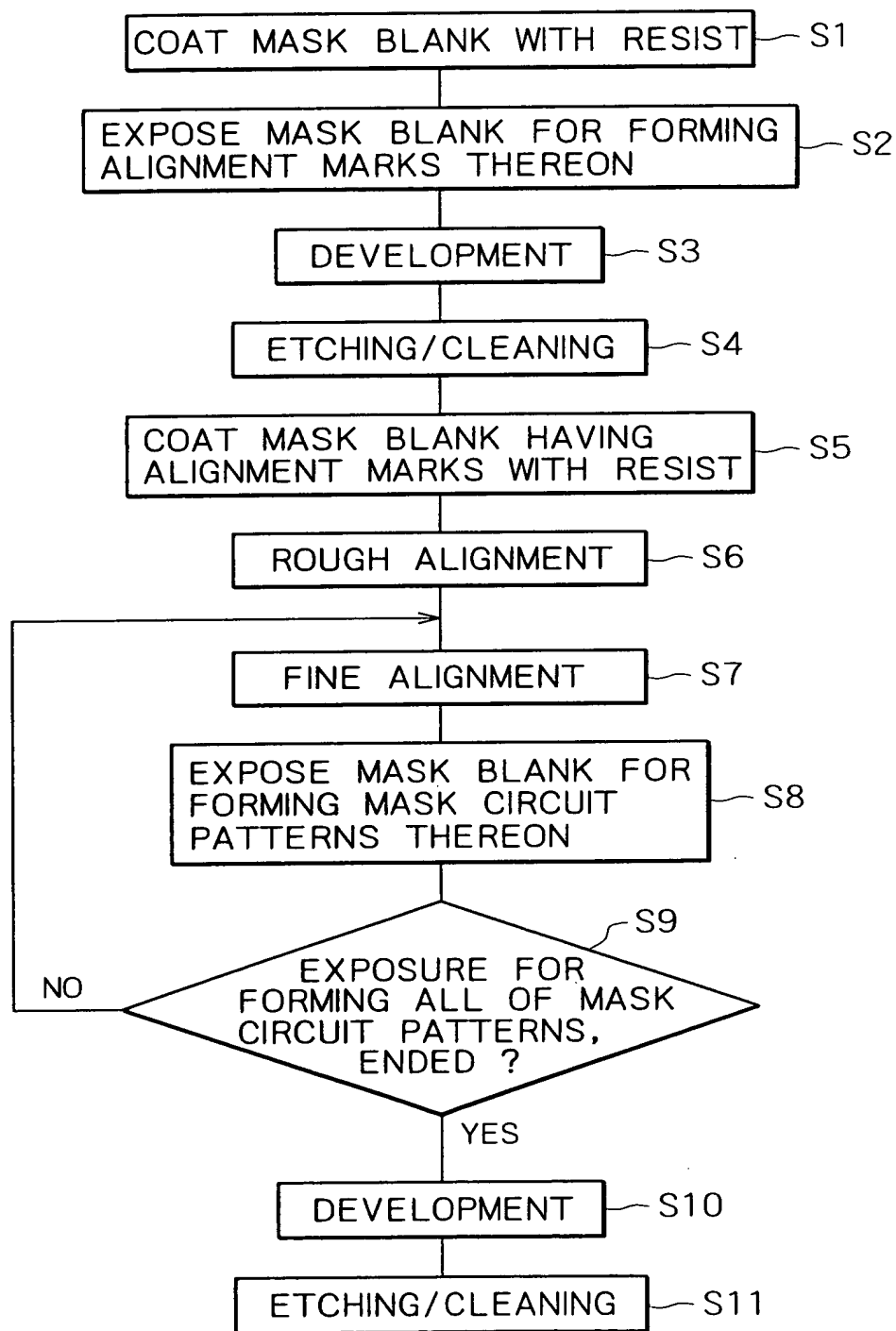


FIG. 4A

REGION IN WHICH MASK CIRCUIT
PATTERN IS TO BE FORMED

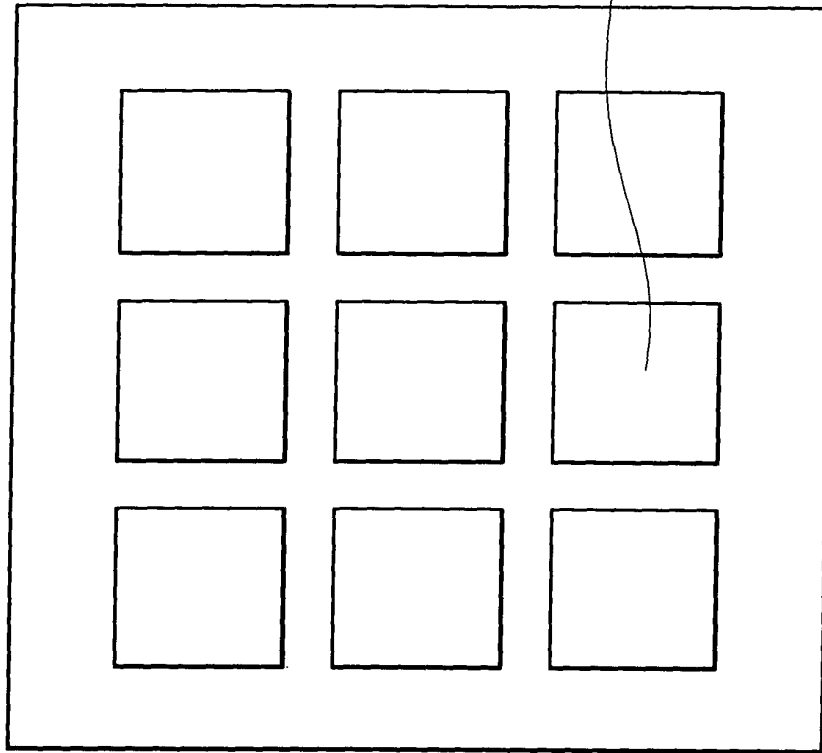


FIG. 4B

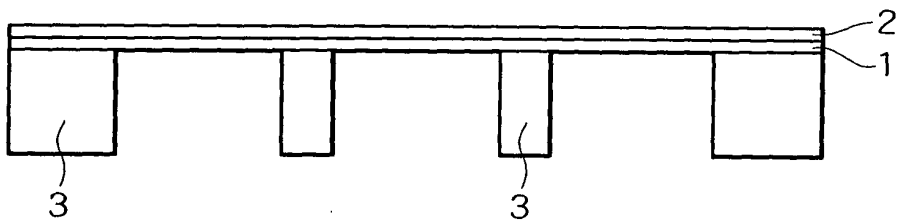


FIG. 4C

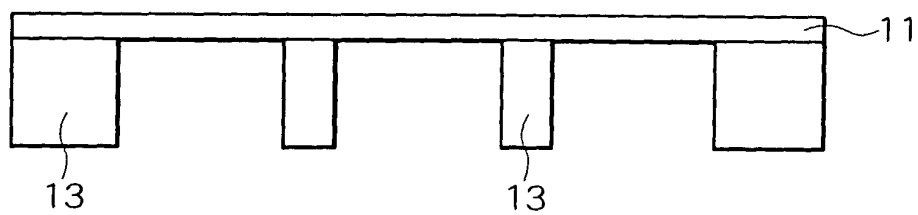


FIG. 6

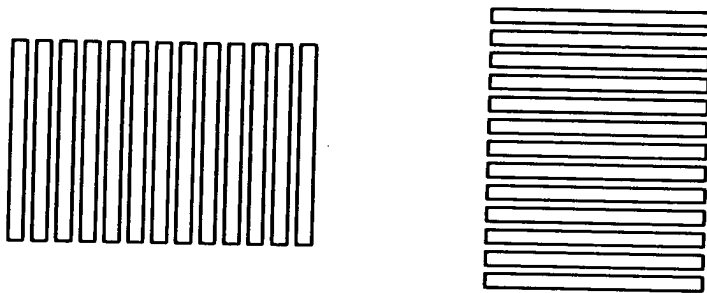


FIG. 7A

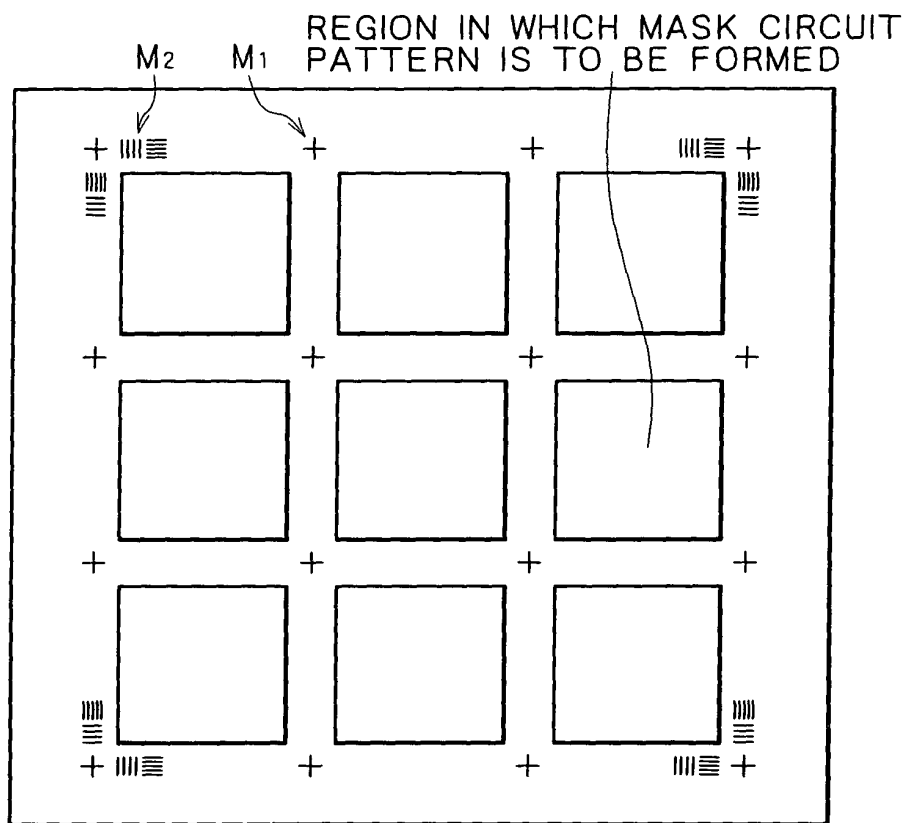


FIG. 7B

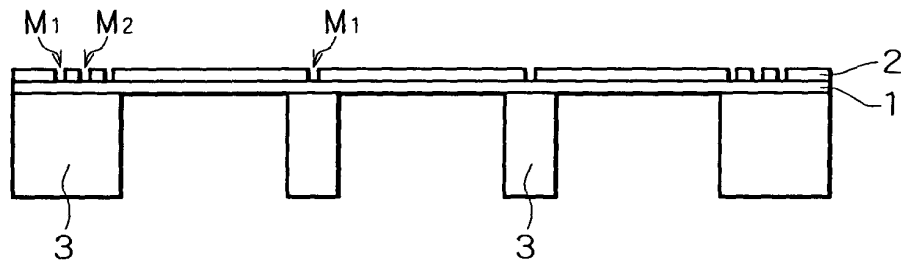


FIG. 7C

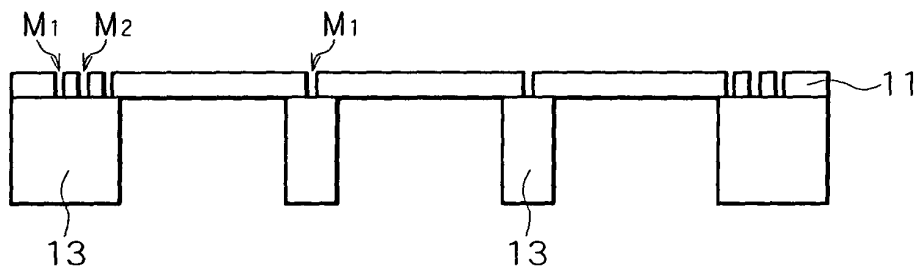


FIG. 8

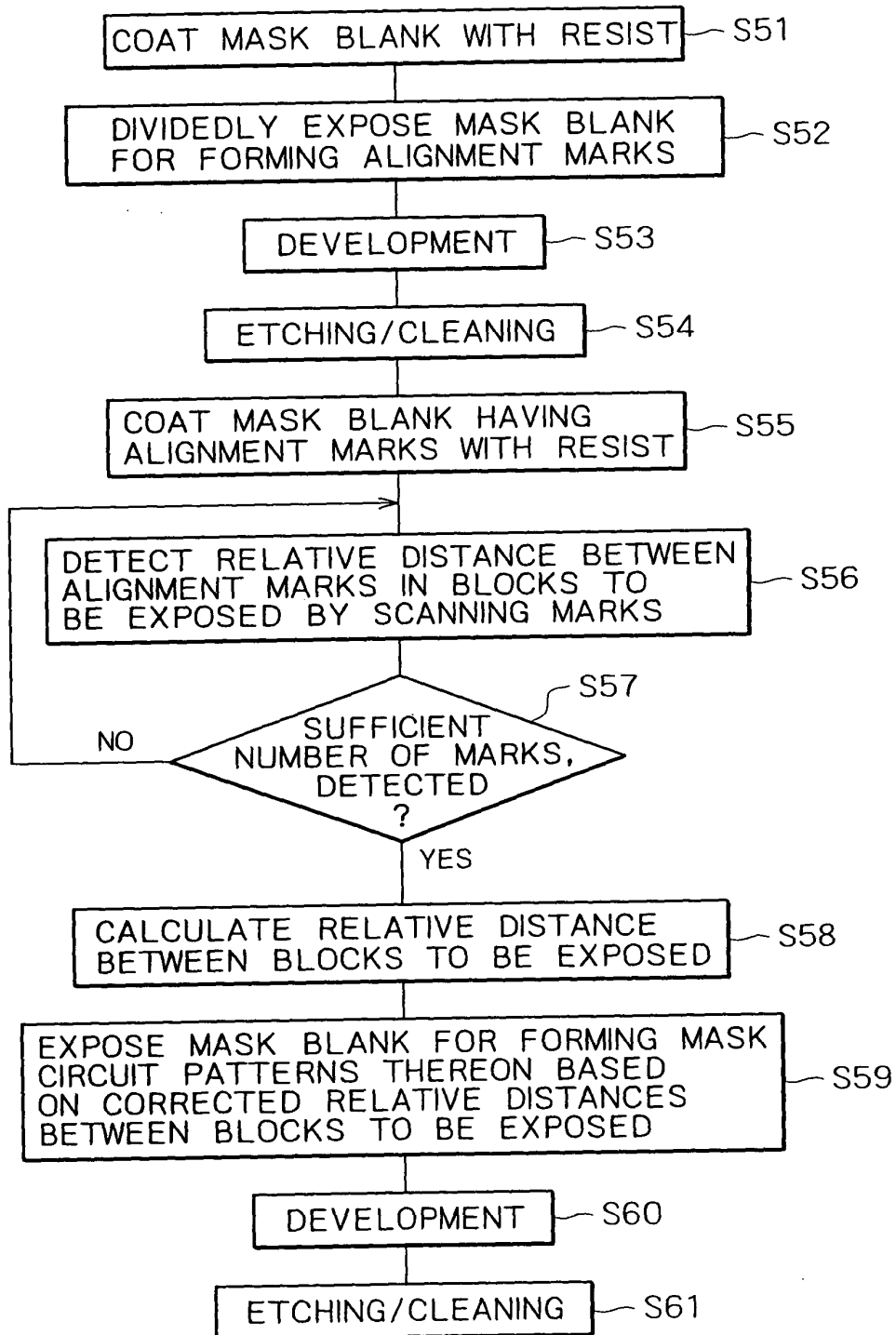


FIG. 9

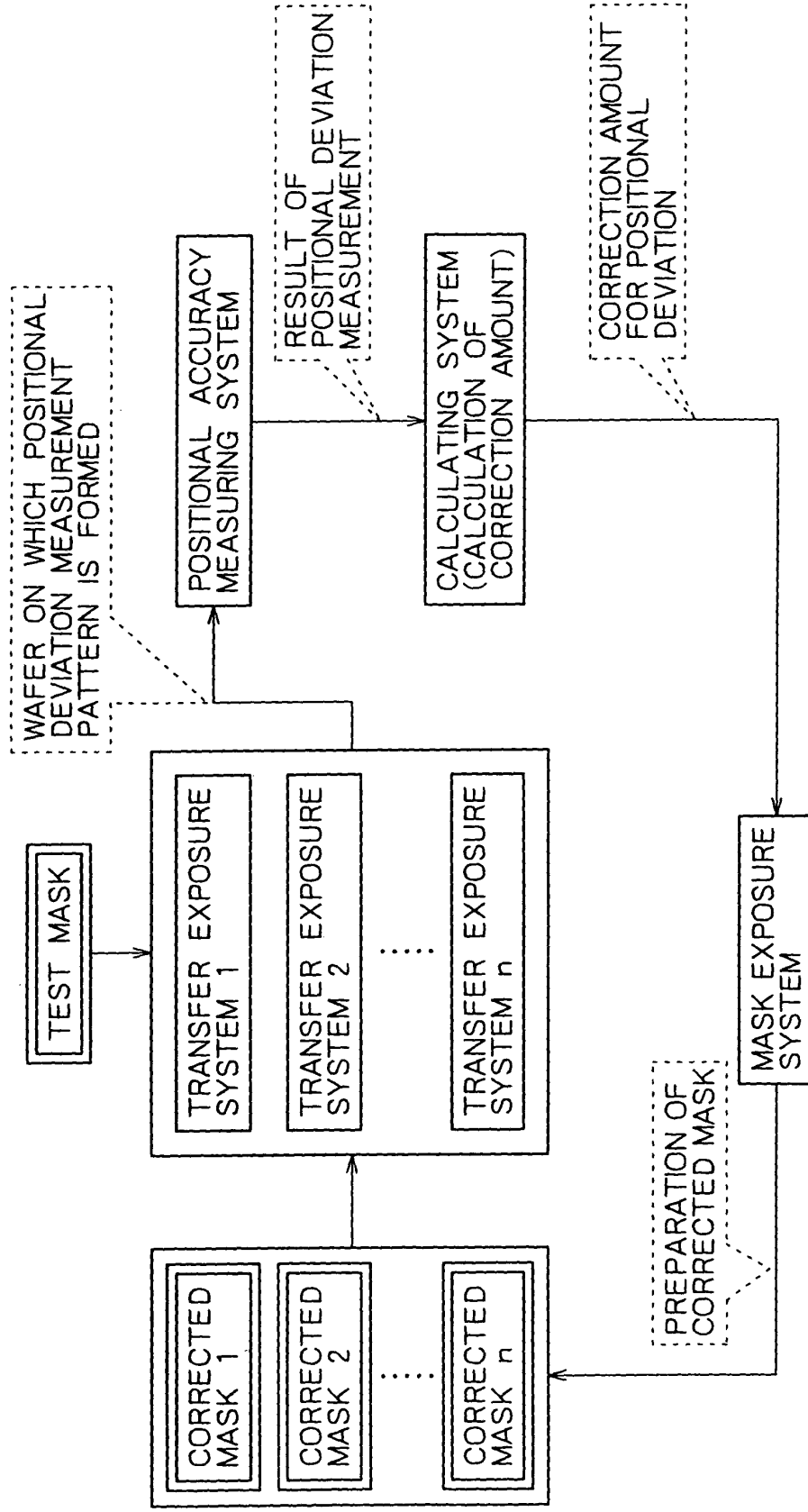


FIG. 10

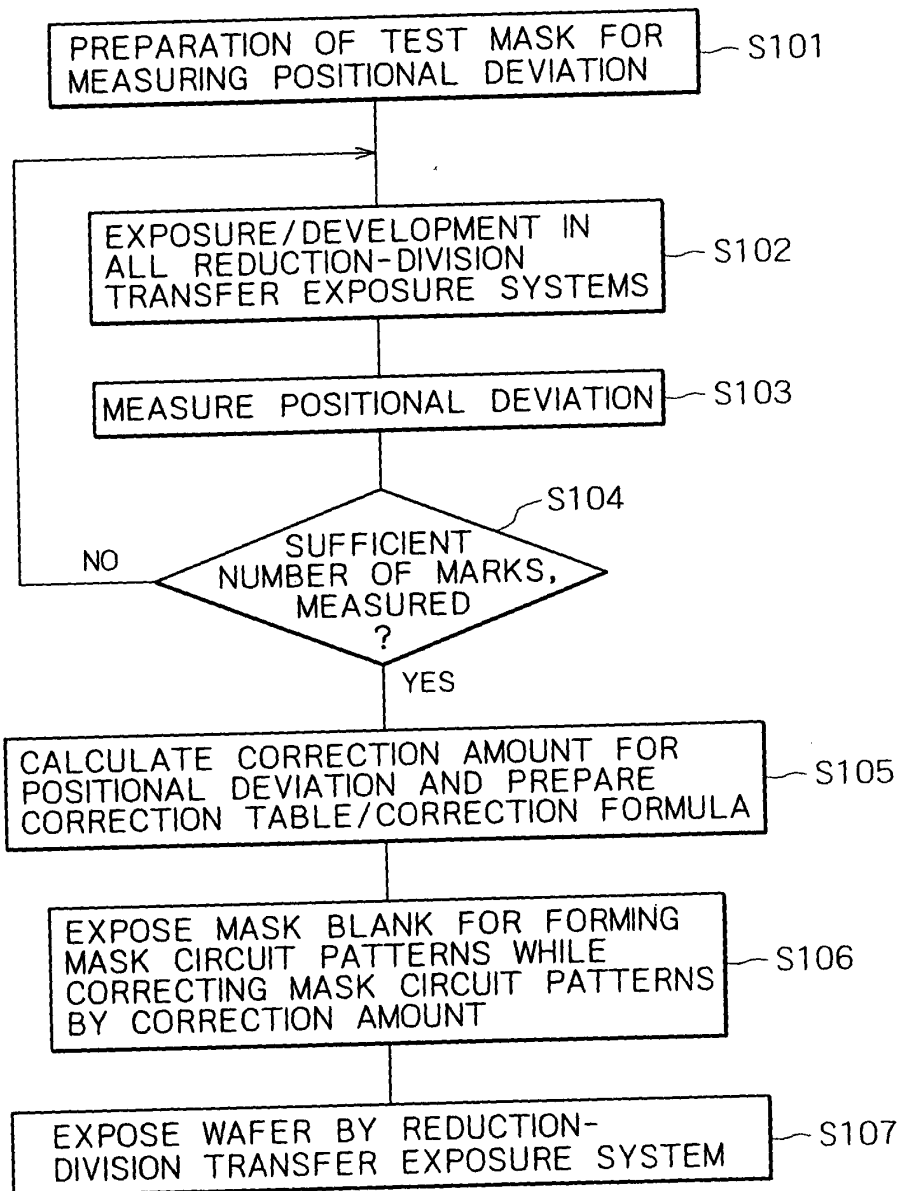


FIG. 11

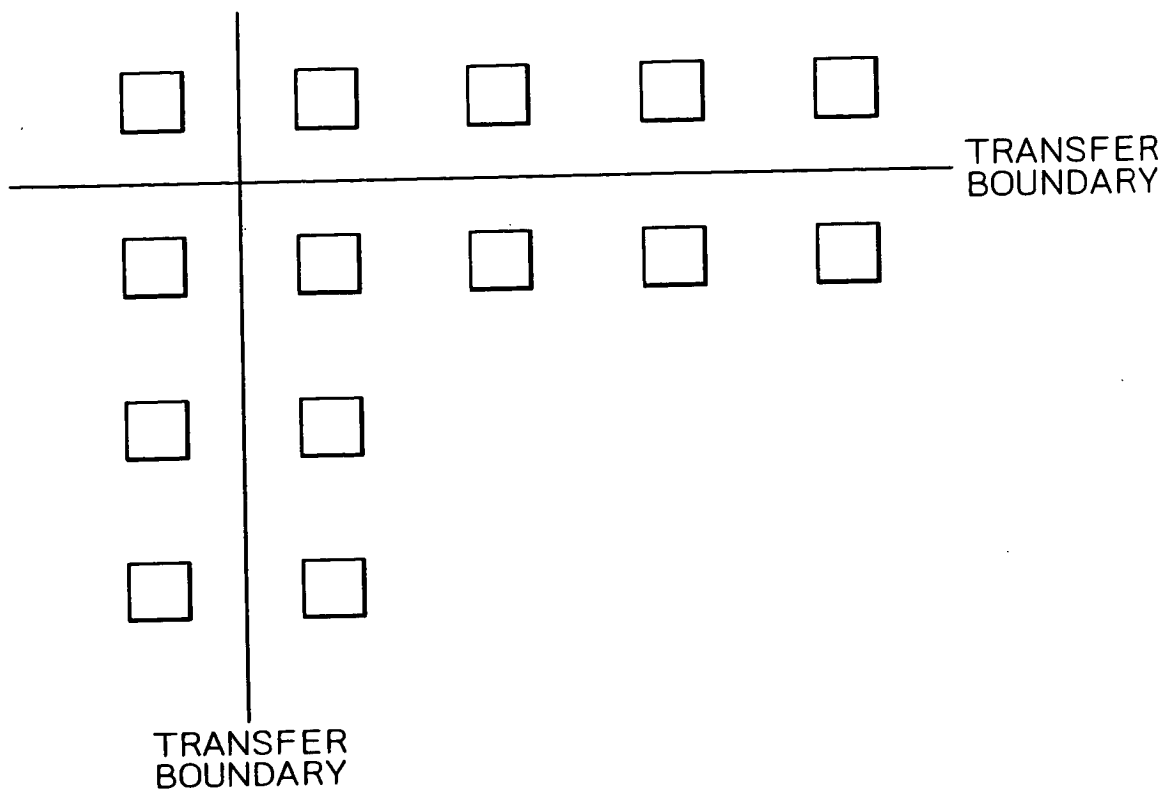


FIG. 12

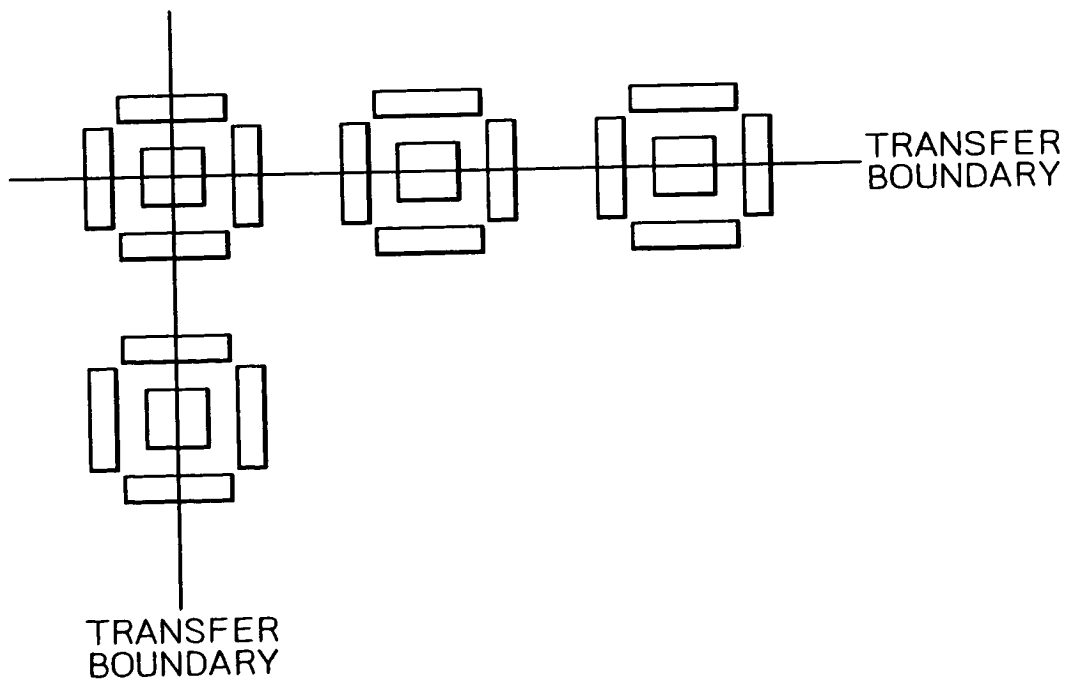


FIG. 13

